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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
TECHNOLOGY CENTER 2000

In re the Application of

Akihiko EBINA et al.

Group Art Unit: 2814

Application No.: 10/052,549

Examiner: M. D. Pizarro Crespo

Filed: January 23, 2002

Docket No.: 111779

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING NONVOLATILE
SEMICONDUCTOR MEMORY DEVICES HAVING CONTROL GATES
CONNECTED TO COMMON CONTACT SECTION (AS AMENDED)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection, Notice of Allowance, or other action that closes prosecution (e.g., Quayle Action), but before payment of the Issue Fee. Attached is our Check No. 145637 in the amount of \$180.00 in payment of the fee under 37 CFR §1.17(p). Please credit or debit Deposit Account No. 15-0461 as needed to ensure consideration of the disclosed information. Two duplicate copies of this paper are attached.

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- ☒ a. I hereby certify that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
- ☒ 2. US 2003/0053345 A1 is from the same patent family as JP-A-2002-203918.

Respectfully submitted,



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Date: August 28, 2003

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